Code No: P21CST03

HALL TICKET NUMBER

PACE INSTITUTE OF TECHNOLOGY & SCIENCES::ONGOLE
(AUTONOMOUS)
II B.TECH I SEMESTER END REGULAR EXAMINATIONS, JAN - 2023
COMPUTER ORGANIZATION
(Common to CSE, CSIT Branches)

Time: 3 hours Max. Marks: 70

Answer all the questions from each UNIT (5X14=70M)

Q.No.		Questions	Marks	CO	KL
		UNIT-I			
1.	a)	Draw and explain the flowchart for instruction cycle.	[7M]	1	3
	b)	Explain addressing modes with an example to each one	[7M]	1	3
		OR			
2.	a)	Explain Data transfer instructions	[7M]	1	2
	b)	Explain about RISC	[7M]	1	2
		UNIT-II			
3.	a)	Draw and explain Logic Micro-operations in detail.	[7M]	2	2
	b)	Explain the operation of three state bus buffers and show its use in design of common bus.	[7M]	2	3
		OR			
4.	a)	Draw and explain control unit of basic computer system.	[7M]	2	2
	b)	Describe the micro programmed control organization and compare its advantages over hardwired control.	[7M]	2	2
		UNIT-III			
5.	a)	Draw a flow chart for Floating point Add/subtract operations	[7M]	3	2
	b)	Multiply -10 and -4 by using Booth Multiplication	[7M]	3	3
		OR			
6.	a)	Explain division algorithm by using example	[7M]	3	2
	b)	Explain BCD addition operation with an example	[7M]	3	2
		UNIT-IV			
7.	a)	Explain in detail the different types of mapping techniques used in cache	[7M]	4	3
	b)	What are the different modes of data transfer or I/O communication techniques?	[7M]	4	1
		OR			
8.	a)	Explain Booth's multiplication algorithm.	[7M]	4	2
	b)	Discuss about memory mapping techniques.	[7M]	4	
		UNIT-V			
9.	a)	Explain about Interrupt Cycle.	[7M]	5	2
	b)	What are the different modes of data transfer or I/O communication?	[7M]	5	2
		OR			
10.	a)	Explain the concept of daisy chaining priority.	[7M]	5	2
	b)	What is Direct Memory Access? Explain the working of DMA	[7M]	5	2

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